



P026

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Drew D. Perkins et al.

Group Art Unit: 2633

Serial No.: 10/712,732

Examiner: None Yet Assigned

Filed: November 12, 2003

Confirmation No. 9275

Title: **METHOD AND APPARATUS FOR DEPLOYING FORWARD
ERROR CORRECTION IN OPTICAL TRANSMISSION
NETWORKS AND THE DEPLOYMENT OF PHOTONIC
INTEGRATED CIRCUIT (PIC) CHIPS WITH THE SAME**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

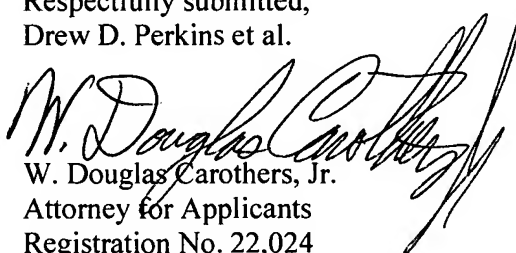
Sir:

Applicants submit herewith documents of which they are aware, which they believe may be material to the examination of this application and in respect to a duty of disclosure in accordance with 37 CFR 1.56. A copy of each reference is enclosed. These references were reviewed and considered in the preparation of this patent application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this Statement, and no representation is being made that a search has been conducted or that this Statement encompasses all the possible relevant information.

The Applicant believes that no fee is required for submission of this Statement because it is being submitted before the mailing date of the first Office Action on the merits in accordance with 37 CFR 1.97(b)(3). However, if a fee is required, the undersigned authorizes the Commissioner to charge the necessary fee, or credit any overpayment, to USPTO Deposit Account No. 502022.

Respectfully submitted,
Drew D. Perkins et al.



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W. Douglas Carothers, Jr.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/712,732
Filing Date	November 12, 2003
First Named Inventor	Drew D. Perkins
Art Unit	2633
Examiner Name	Not Yet Assigned
Attorney Docket Number	P026

Sheet	1	of	1
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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